

Notic of Referenc s Cit d	Application/Control No. 09/696,955	Applicant(s)/Patent Under Reexamination TAMURA ET AL.	
	Examiner Matthew A. Anderson	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification	
	A	US-6284093 B1	09-2001	Ke et al.	156	345
	B	US-6299982 B1	10-2001	Tamatsuka et al.	428	446
	C	US-6190495 B1	02-2001	Kubota et al.	156	345
	D	US-				
	E	US-				
	F	US-				
	G	US-				
	H	US-				
	I	US-				
	J	US-				
	K	US-				
	L	US-				
	M	US-				

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification	
	N						
	O						
	P						
	Q						
	R						
	S						
	T						

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: rocess Technology, Lattice Press, Sunset Beach, CA, USA, pp. 8, 23-27, 32-33, 59, 1986.					
	V						
	W						
	X						

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.